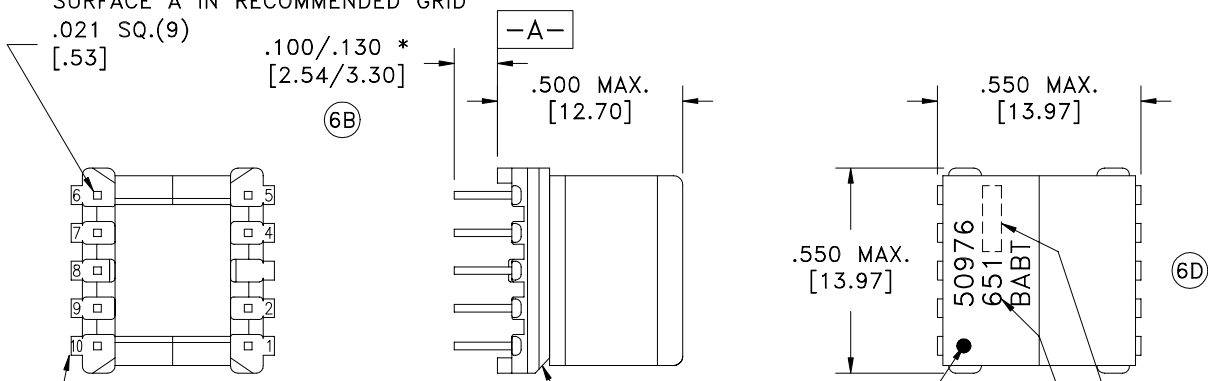


PART MUST INSERT FULLY TO SURFACE A IN RECOMMENDED GRID
 .021 SQ.(9)
 [.53]

* DIMENSION MAY BE EXCEEDED WITH SOLDER ONLY

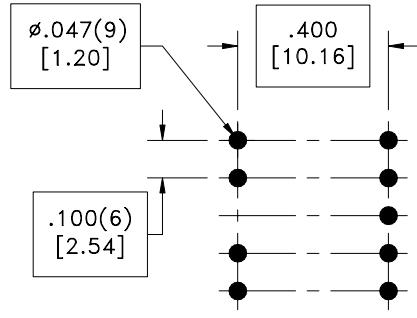
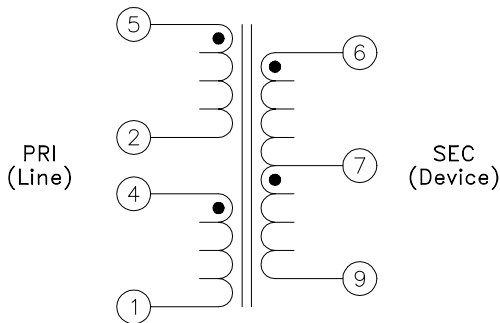


CHAMFERS LOCATE TERM.'s #1 & 10
 TERM. NO.'s FOR REF. ONLY

DOT LOCATES TERM. #1

EIA CODE

LOT CODE & DATE CODE



RECOMMENDED P.C. PATTERN, COMPONENT SIDE

ELECTRICAL SPECIFICATIONS @ 25°C unless otherwise noted:

- LONGITUDINAL BALANCE: 55dB min., 20k - 560kHz, per ITU Method (L->M).
- D.C. RESISTANCE (@20°C):
 - 5-2, 1.87 ohms ±10%.
 - 4-1, 1.87 ohms ±10%.
 - 6-9, 0.61 ohms ±10%.
- 6B DIELECTRIC RATING: 2000VAC, 1 minute tested by applying 2500VAC for 1 second between 5-6; 4-6.
- INDUCTANCE: 2mH ±10%, 10kHz, 1.0VAC, 0/100mADC, 5-1(tie 2+4), Ls.
- LEAKAGE INDUCTANCE: 35uH max., 100kHz, 100mVAC, 5-1(tie 2+4, 6+9), Ls.
- RETURN LOSS: 15dB min., 50k - 200kHz, 15 ohm load, tie(2+4).
- TURNS RATIO: (5-1):(6-9) = 3:1, tie(2+4), ±1%.
- TOTAL HARMONIC DISTORTION: -80dB max., 20kHz, 4.95Vrms, 15 ohm load, 135 ohm input, tie(2+4).
- INTERWINDING CAPACITANCE: 60pF max., 100kHz, 100mVAC, 5-6(tie 2+4), Cs.
- OPERATING TEMPERATURE RANGE: -40°C to +85°C.

Approved for use with Level One's LXT772 HDSL2 chip set.

- 6B 6C Designed to comply with the following requirements as defined by IEC60950, EN60950, UL60950/CSA60950 and AS/NZS60950:
 - Supplementary insulation for a primary circuit at a working voltage of 250Vrms.
 - Reinforced insulation for a secondary circuit at a working voltage of 150VDC.

DETAILS SUBJECT TO CHANGE

6B AGENCY NUMBER	
6C BAPT	NC/005587
IEC 950 (Via CB cert.)	GB539W
ACA/AUSTEL (Via CB cert.)	GB539W
JAPAN (Via CB cert.)	GB539W

Midcom, Inc.
 Watertown, SD USA
 Toll Free: 800-643-2661
 Fax: 605-886-4486

Unless otherwise specified:
 Tolerances: Fractions: ±1/64
 Angles: ±1° Decimals: ±.005[.13]

DRAWING TITLE
TRANSFORMER

Midcom

DRAWING NO. **50976** REV. 6D
 12/03

REVISIONS: SEE SHEET 1

SCALE --- SHEET 2 OF 6

This drawing is dual dimensioned.
 Dimensions in brackets are in millimeters.

DWG # 50976 6B